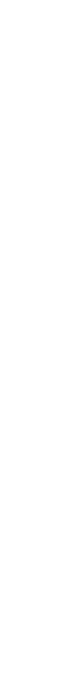
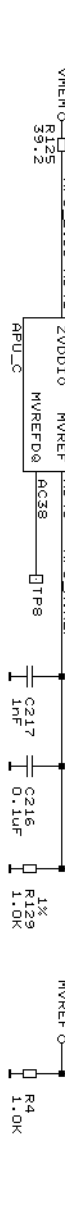
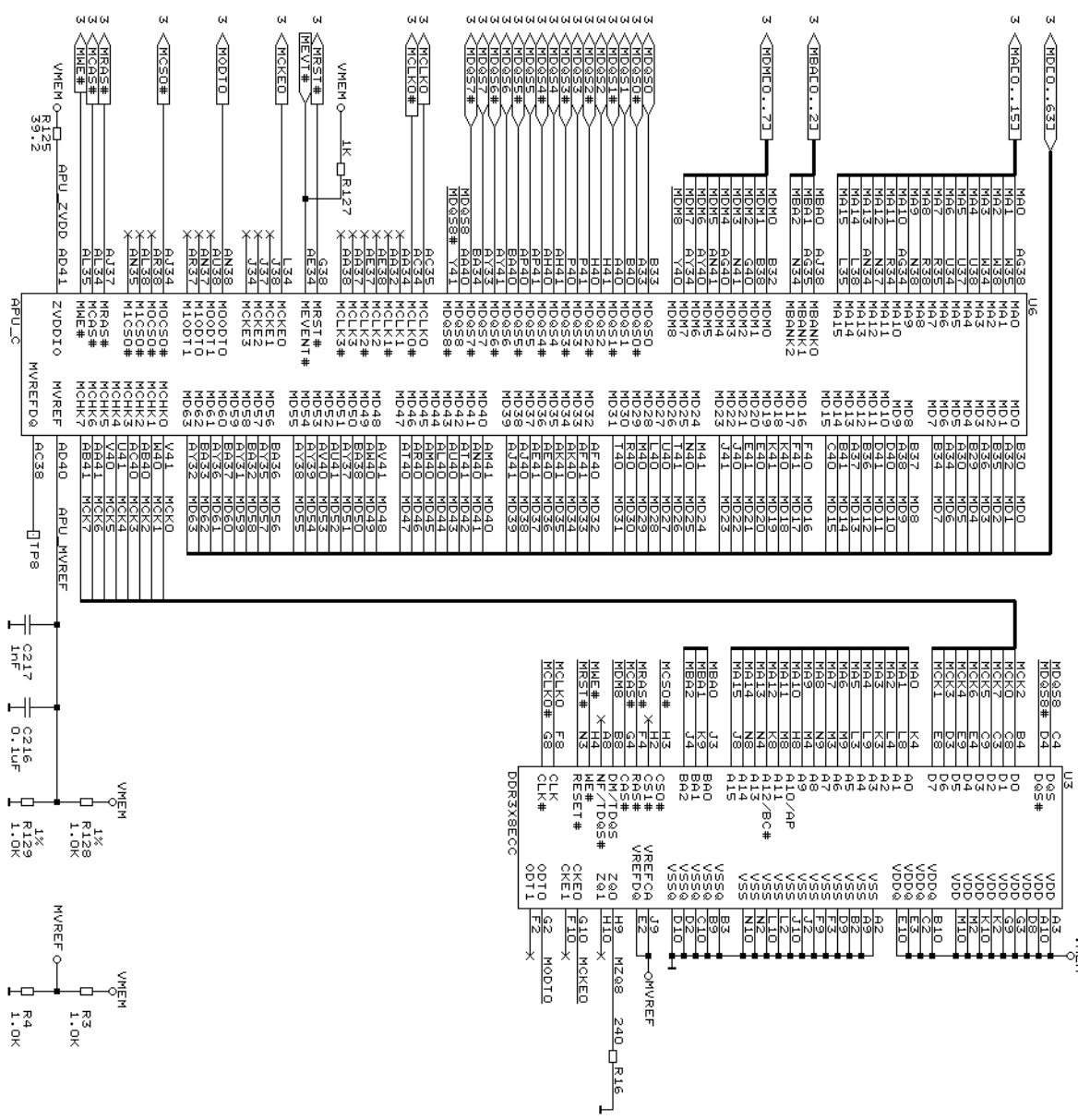
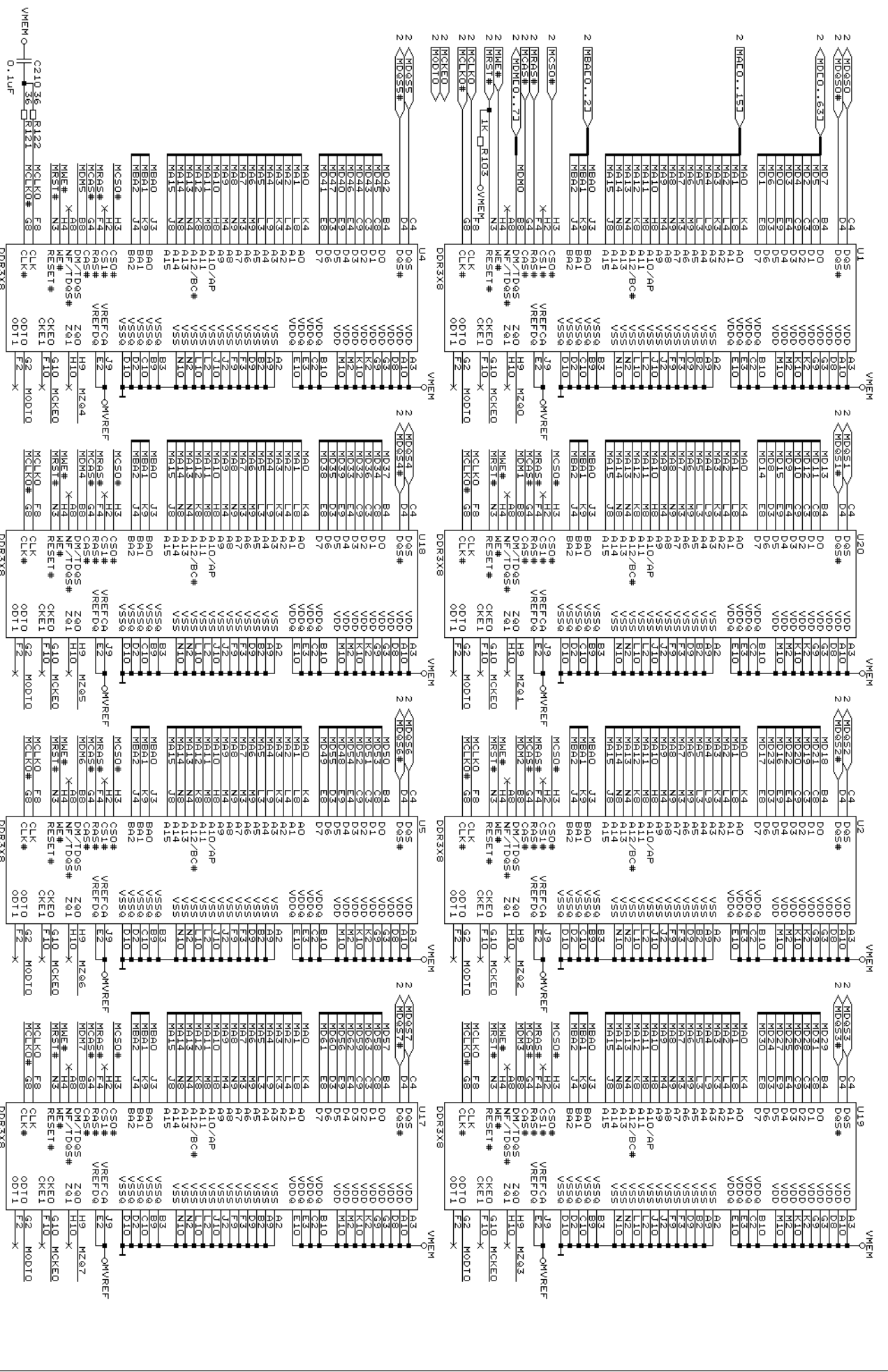




APU DRAM Interface

ECC DRAM



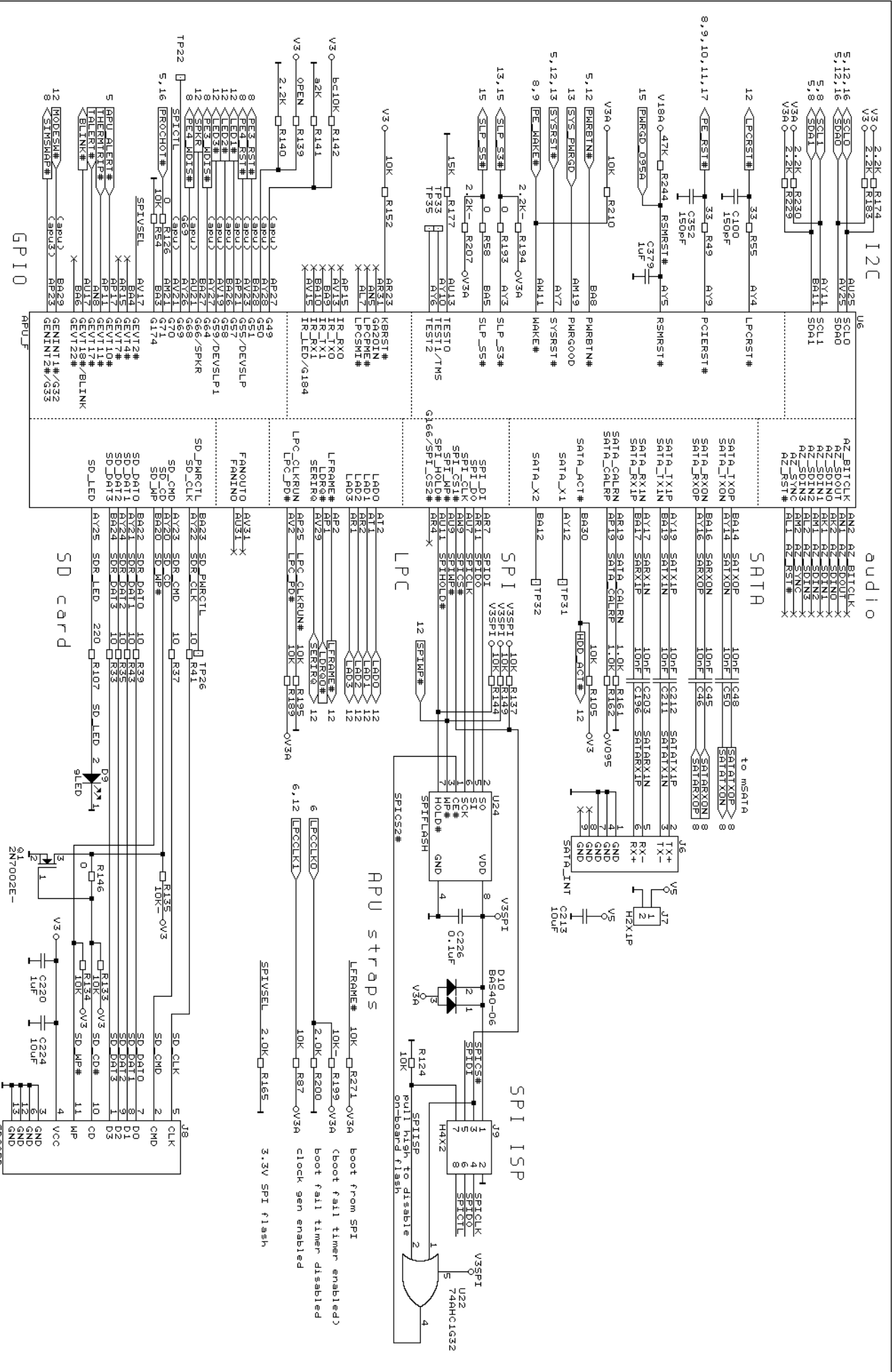


Bank	Module	Bank	Module	Bank	Module	Bank	Module
U11	M0	U11	M8	U11	M16	U11	M24
U11	M1	U11	M9	U11	M17	U11	M25
U11	M2	U11	M10	U11	M18	U11	M26
U11	M3	U11	M11	U11	M19	U11	M27
U11	M4	U11	M12	U11	M20	U11	M28
U11	M5	U11	M13	U11	M21	U11	M29
U11	M6	U11	M14	U11	M22	U11	M30
U11	M7	U11	M15	U11	M23	U11	M31









Component	Signal	Value	Notes
I2C	V3_0	2.2K R124	
	V3_0	2.2K R146	
	V3_0	10K R152	
	V3_0	10K R141	
	V3_0	10K R139	
	V3_0	2.2K R140	
	V3_0	10K R140	
	V3_0	10K R140	
	V3_0	10K R140	
	V3_0	10K R140	
audio	AN2	AV17	
	AN1	AV17	
	AN0	AV17	
	AV17	AV17	
	AV17	AV17	
	AV17	AV17	
	AV17	AV17	
	AV17	AV17	
	AV17	AV17	
	AV17	AV17	
SATA	SATA_TX0P	AV19	
	SATA_RX0N	AV19	
	SATA_TX0P	AV19	
	SATA_RX0N	AV19	
	SATA_TX0P	AV19	
	SATA_RX0N	AV19	
	SATA_TX0P	AV19	
	SATA_RX0N	AV19	
	SATA_TX0P	AV19	
	SATA_RX0N	AV19	
LPC	LPC_CLKRUN	AV22	
	LPC_PIO#	AV22	
	LPC_CLKRUN	AV22	
	LPC_PIO#	AV22	
	LPC_CLKRUN	AV22	
	LPC_PIO#	AV22	
	LPC_CLKRUN	AV22	
	LPC_PIO#	AV22	
	LPC_CLKRUN	AV22	
	LPC_PIO#	AV22	
APU straps	LFRAME#	AV21	
	LDR#	AV21	
	SERIRQ	AV21	
	LFRAME#	AV21	
	LDR#	AV21	
	SERIRQ	AV21	
	LFRAME#	AV21	
	LDR#	AV21	
	SERIRQ	AV21	
	LFRAME#	AV21	
SPI ISP	SPICLK	AV19	
	SPID0	AV19	
	SPID1	AV19	
	SPICLK	AV19	
	SPID0	AV19	
	SPID1	AV19	
	SPICLK	AV19	
	SPID0	AV19	
	SPID1	AV19	
	SPICLK	AV19	

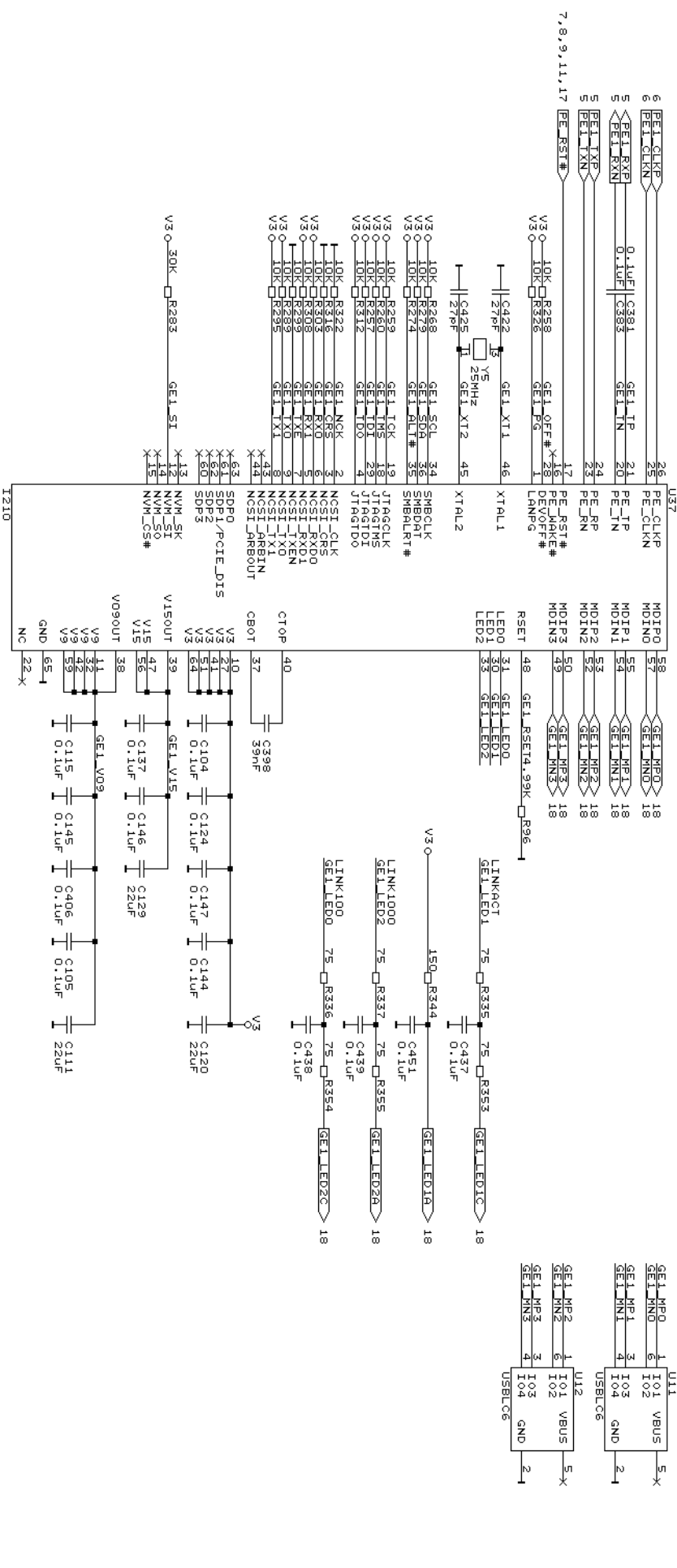






# Gigabit Ethernet 2

# ESD protection



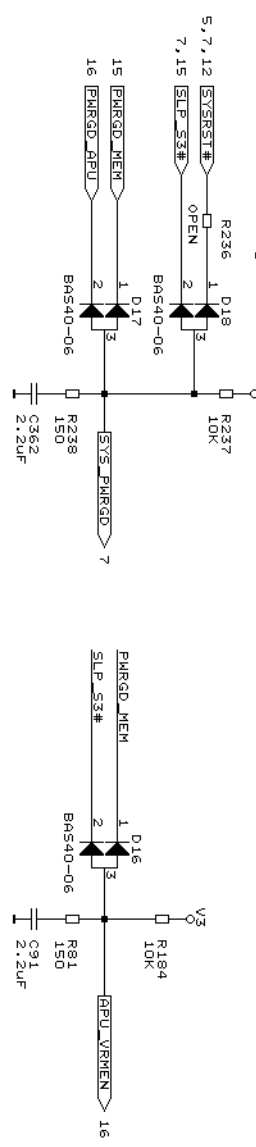
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Size/Document Number		APU	
Date:		June 8, 2016/Sheet 10 of 18	
REV		48	

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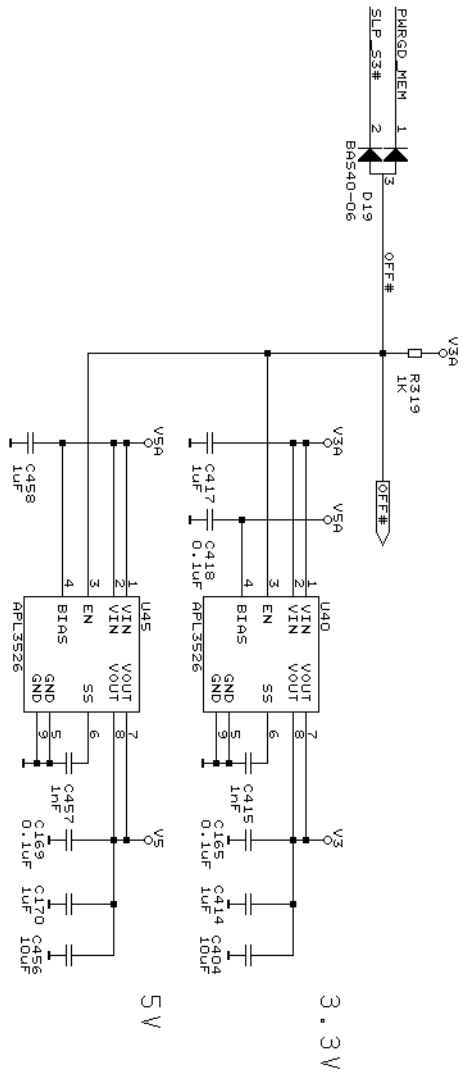
Power good v18a



VRM test points

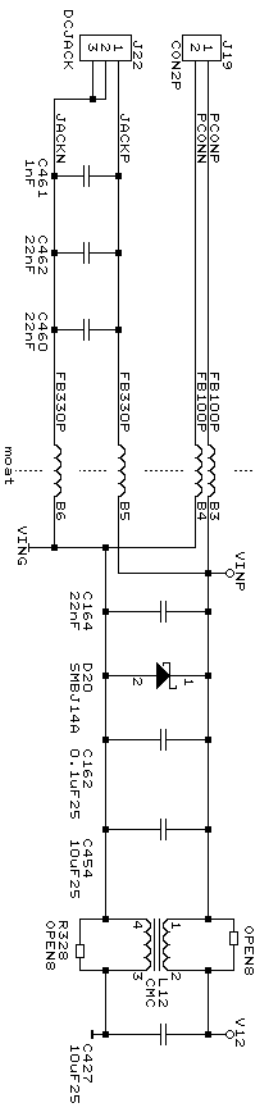
- V5A-O-TP46
- V3A-O-TP44
- V18A-O-TP37
- V15A-O-TP40
- VMEM-O-TP6
- V095A-O-TP42
- VCORE-O-TP38
- V5-O-TP45
- V3-O-TP43
- V18-O-TP36
- VT1-O-TP7
- V095-O-TP41
- VNB-O-TP39

Power sequence:  
Turn on after VMEM is good.

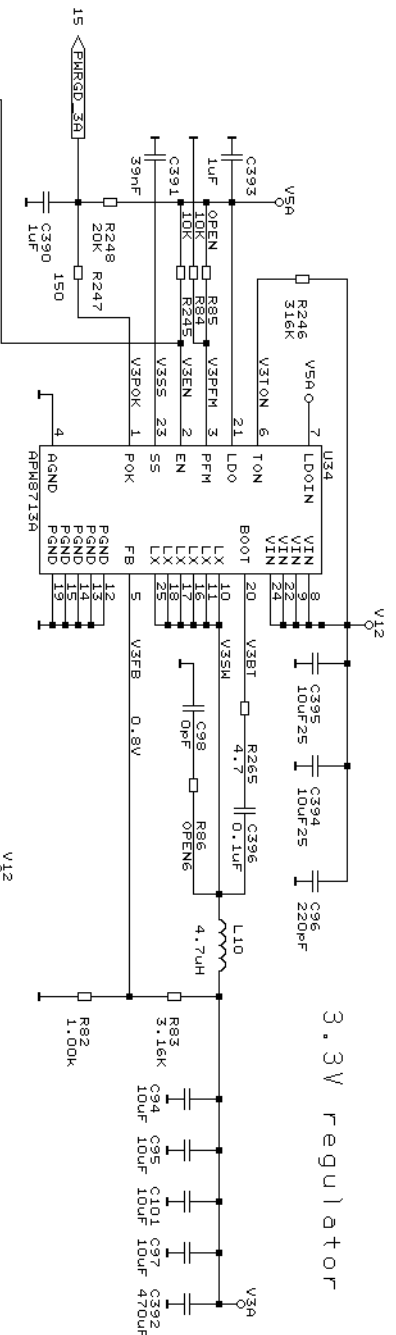


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Title	power good, power switch
Size	Document Number
B	APU
Date:	December 7, 2016
Sheet	13 of 18
REV	48

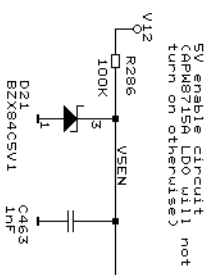
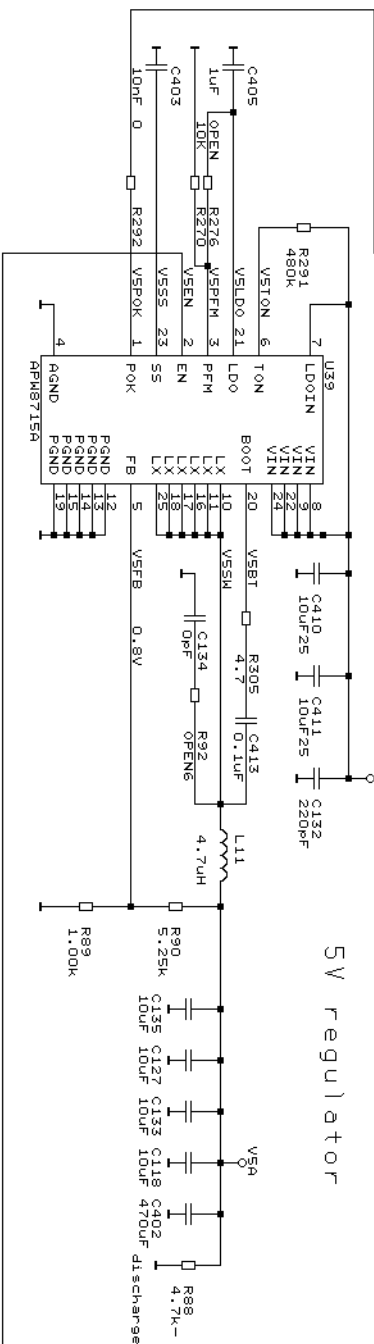
Power input +12V



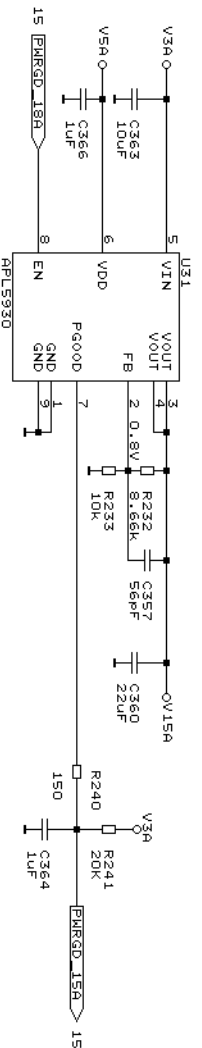
3.3V regulator

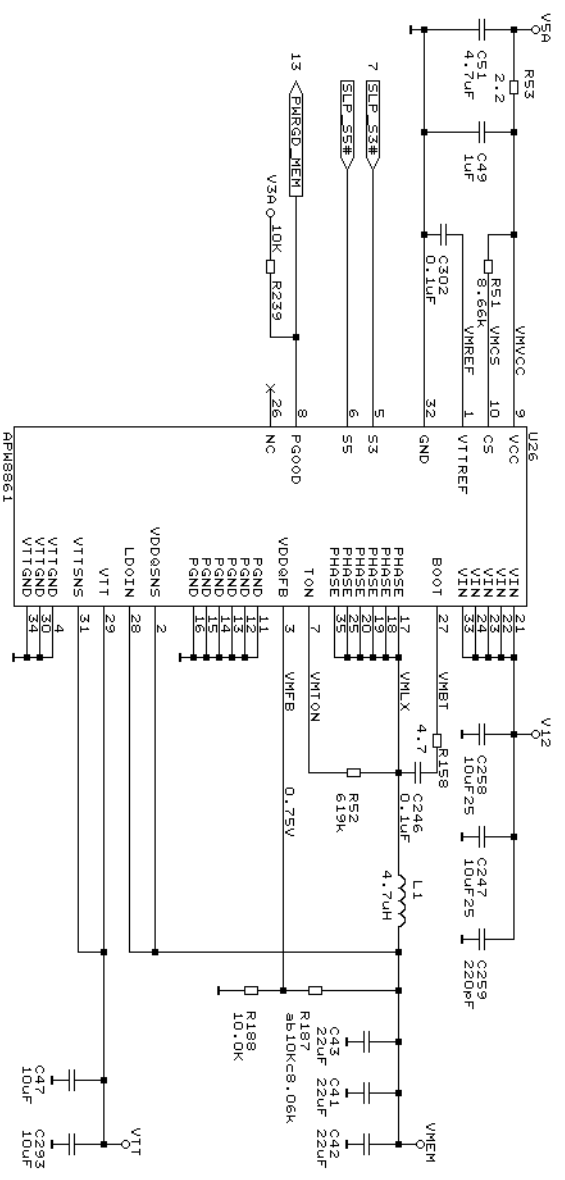
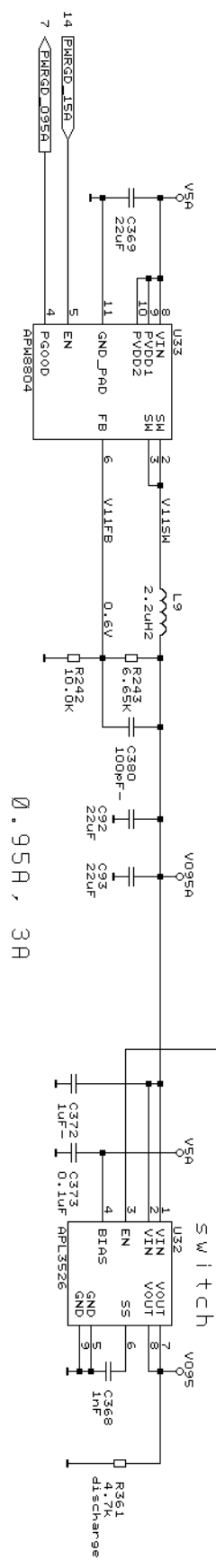
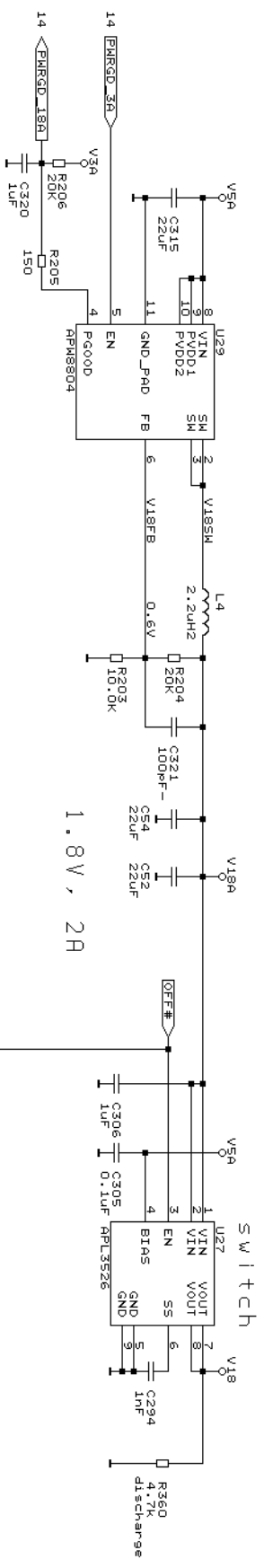


5V regulator



3.3V -> 1.5V LDO (0.5A)





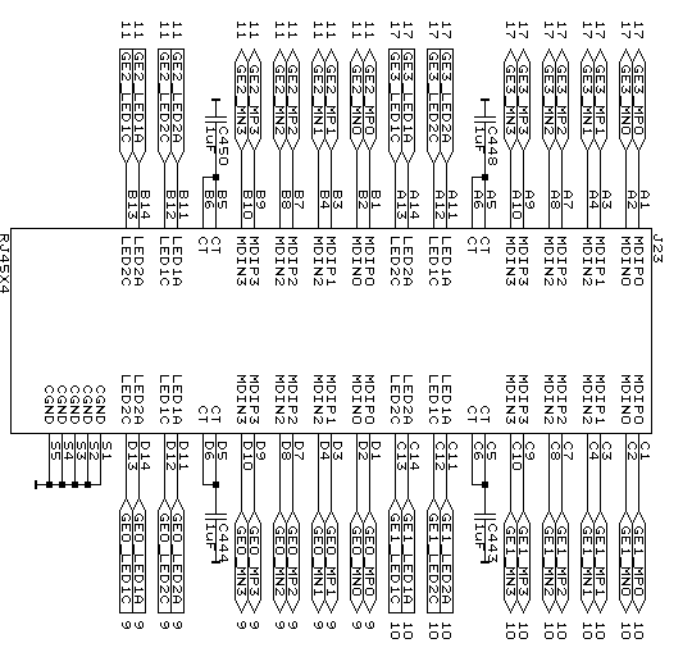
1.5V / 1.35V VMEM  
 0.75V / 0.675V VTT

Title		CC202016 PC Engines GmbH	
Size		1.8V, 0.95V, VMEM, VTT	
Document Number		APU	
Date		January 19, 2017	
Sheet		15 of 48	









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Title Ethernet connector	
Size	Document Number APU
REV	48
Date:	June 7, 2016 Sheet 18 of 18