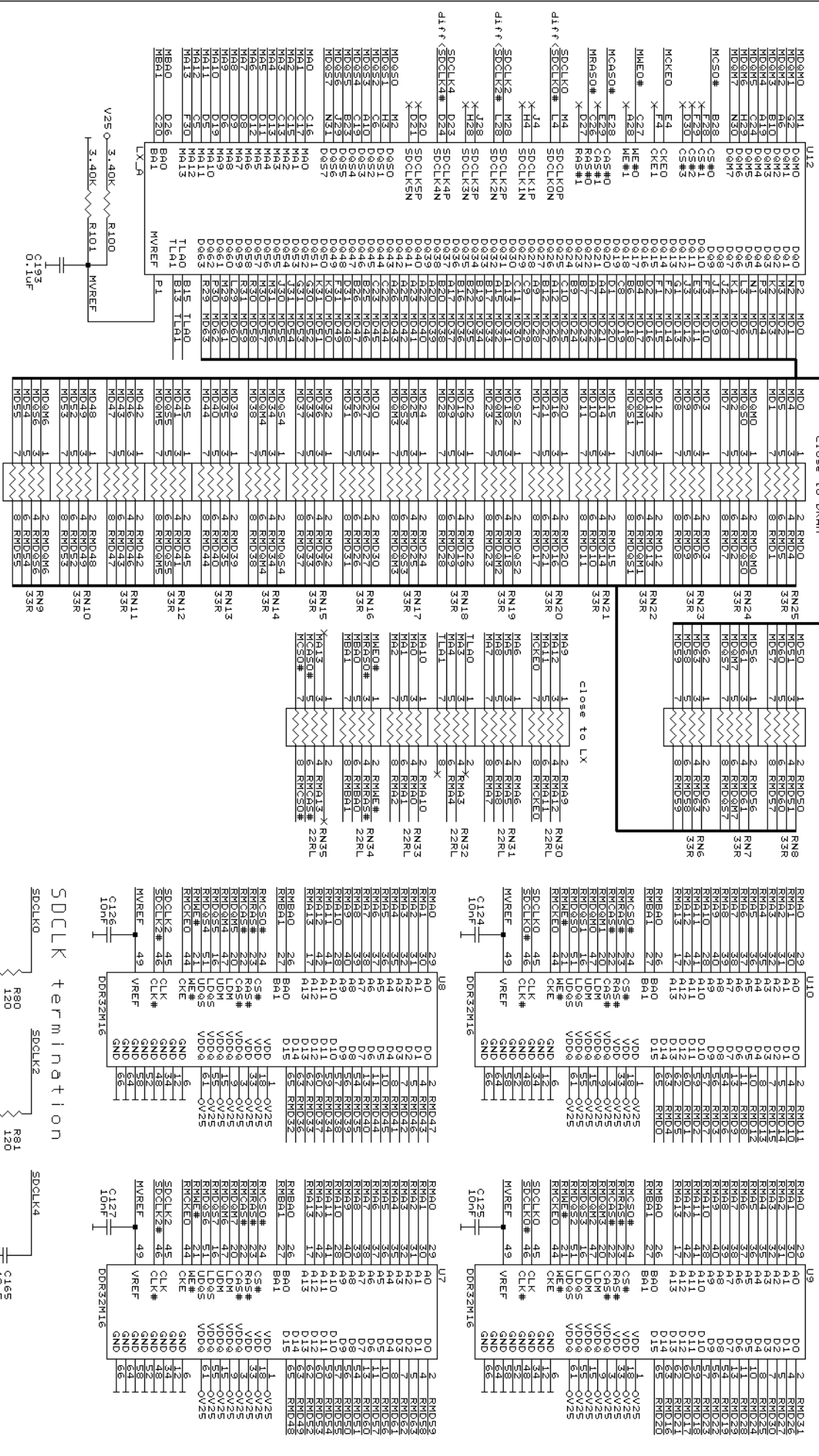


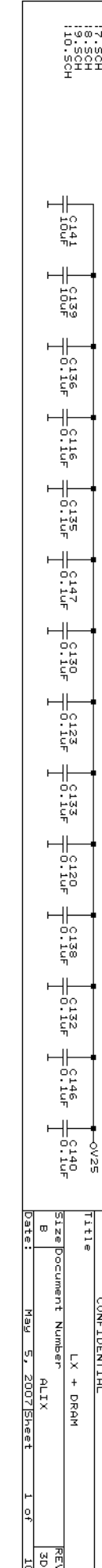
LX memory controller

128MB or 256MB DDR DRAM (PC400)

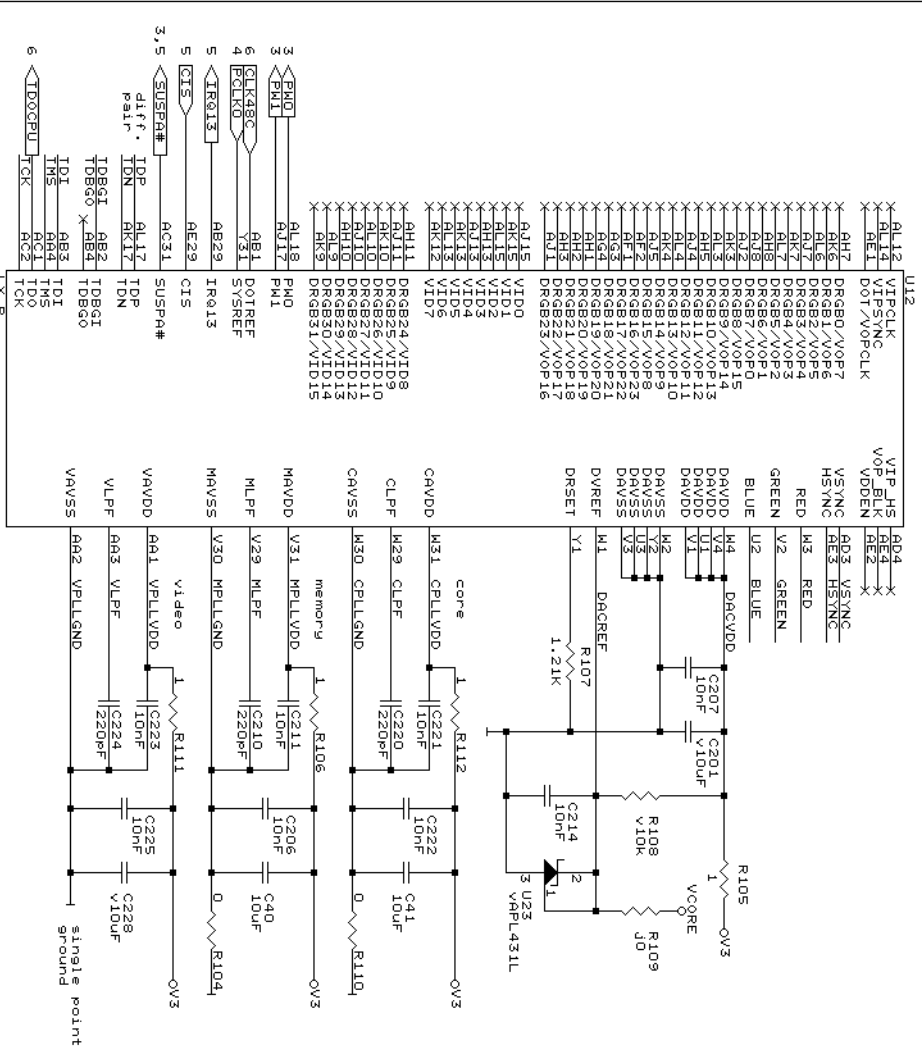
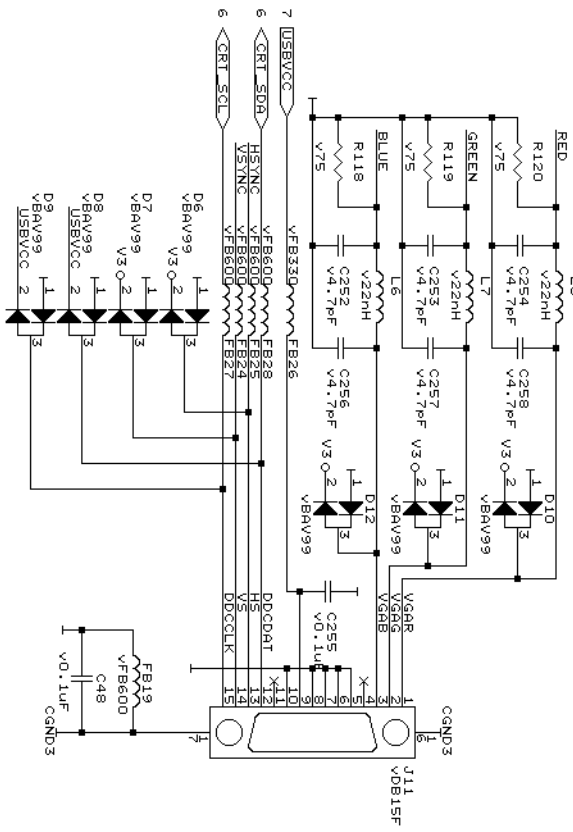


close to DRAM

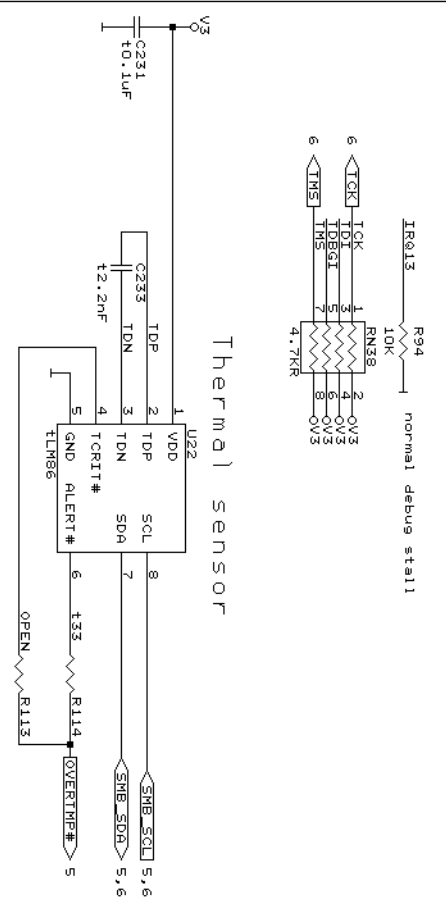
close to LX



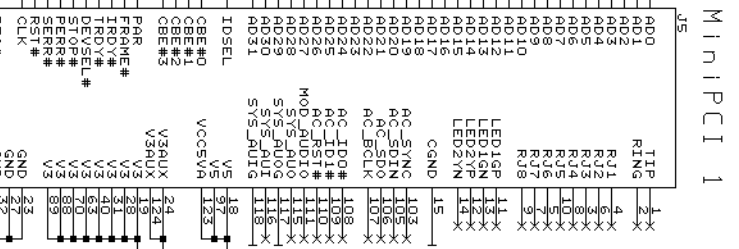
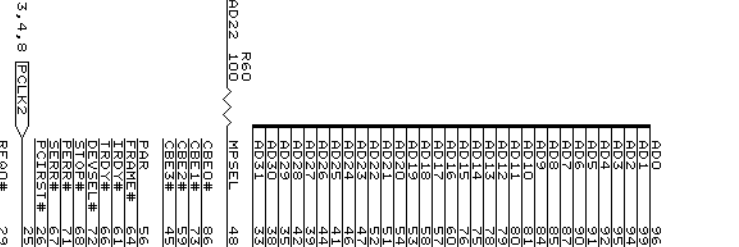
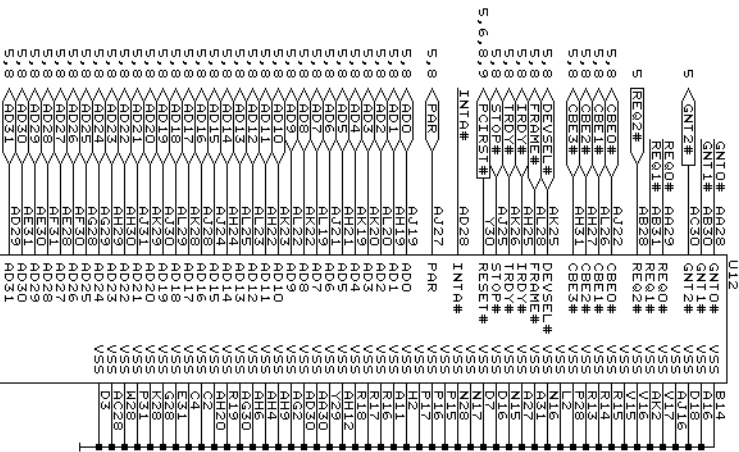
### VGA port (option V)



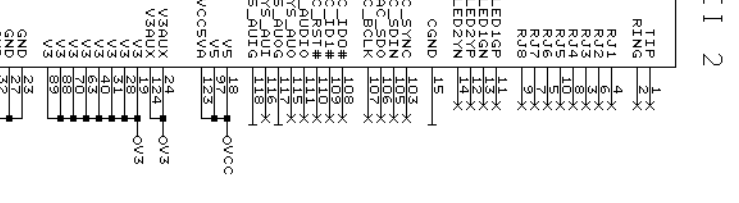
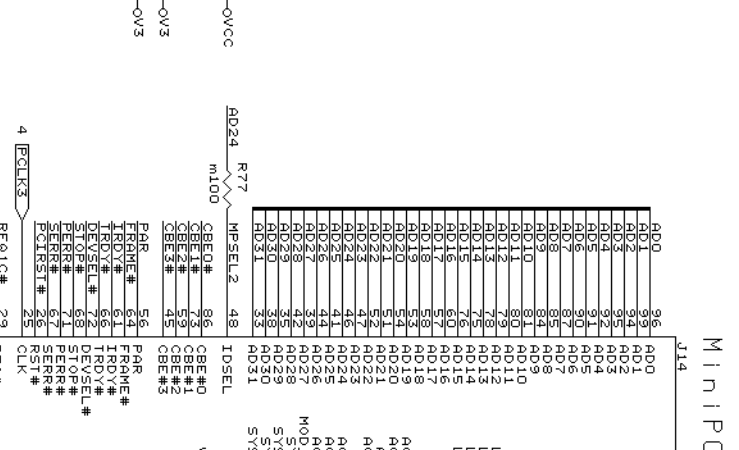
### Thermal sensor



MiniPCI 1

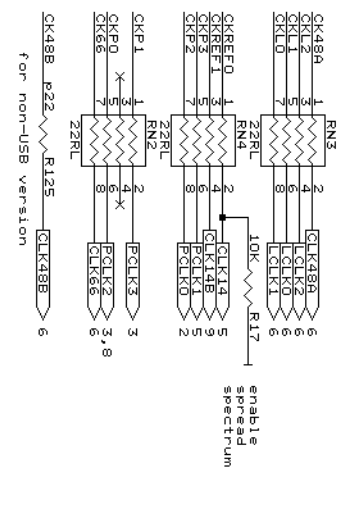
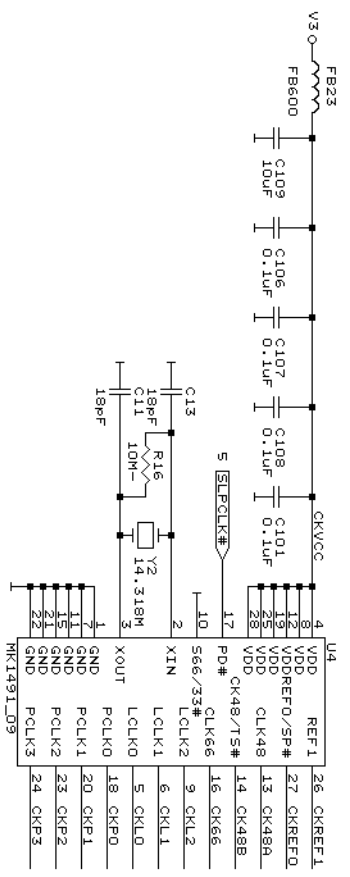
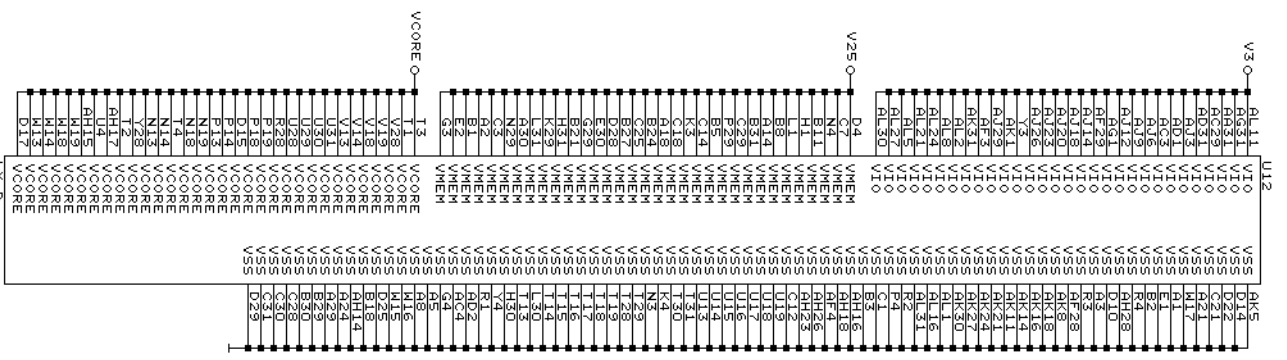


MiniPCI 2

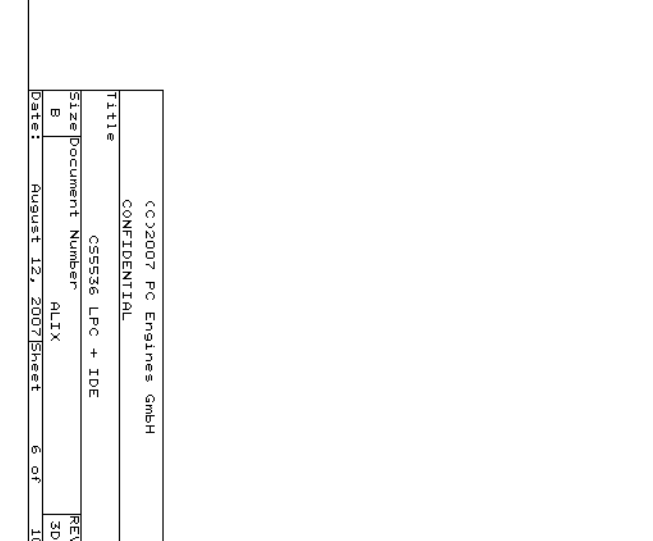
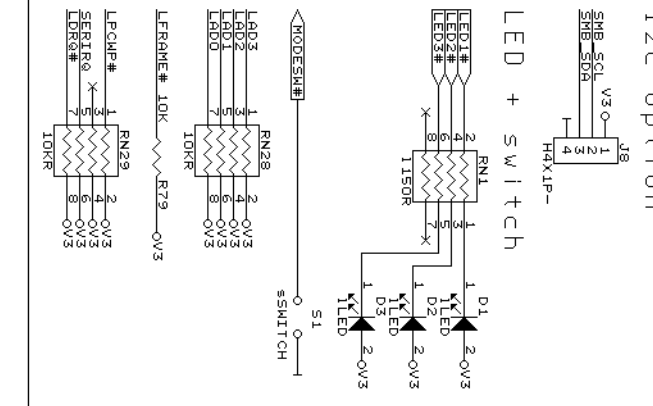
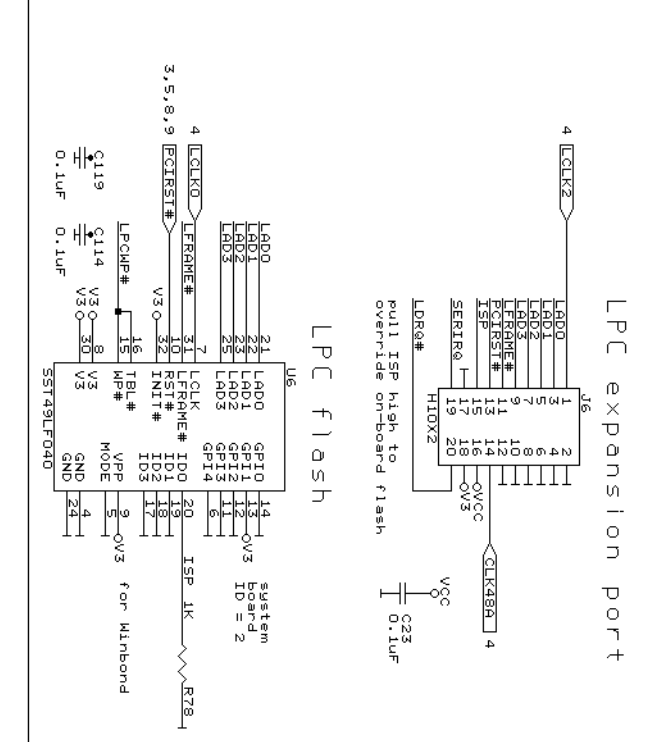
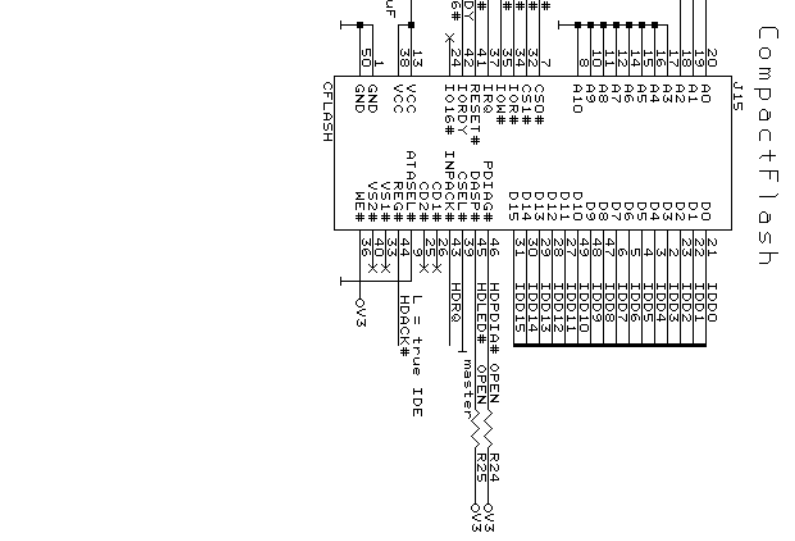
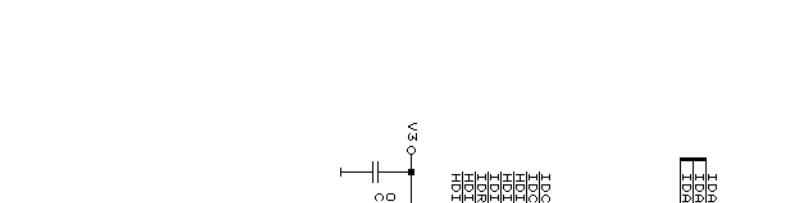
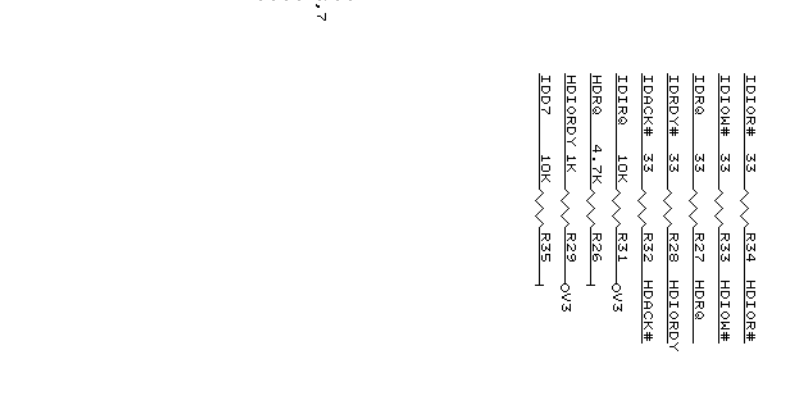
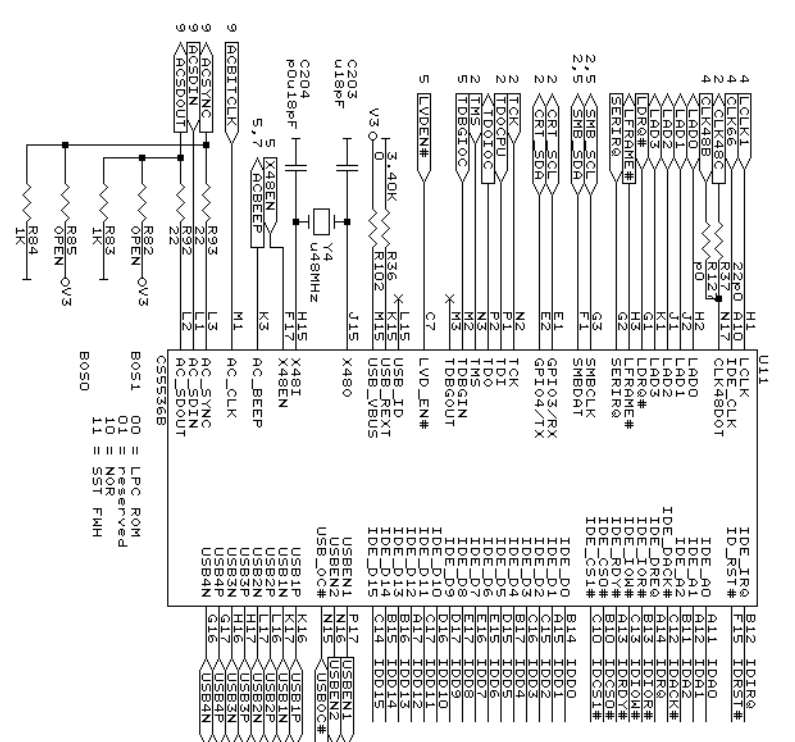


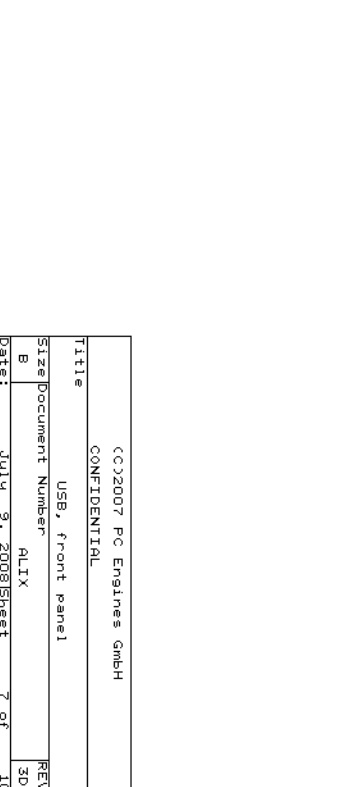
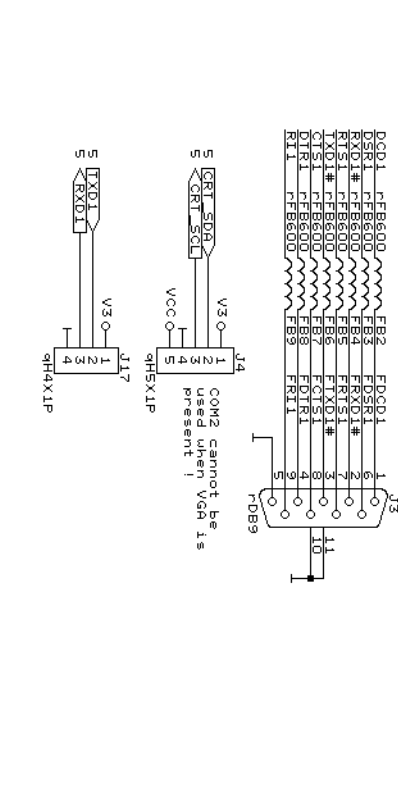
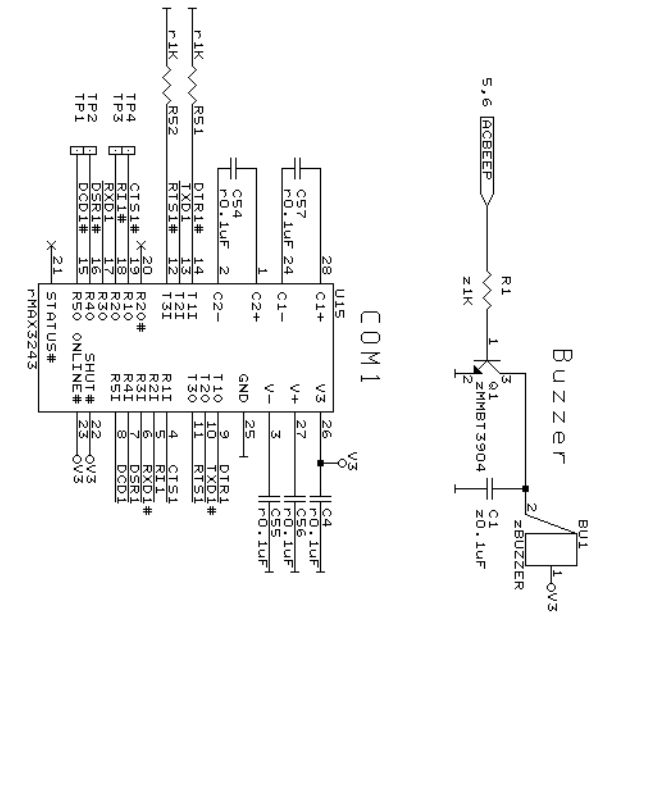
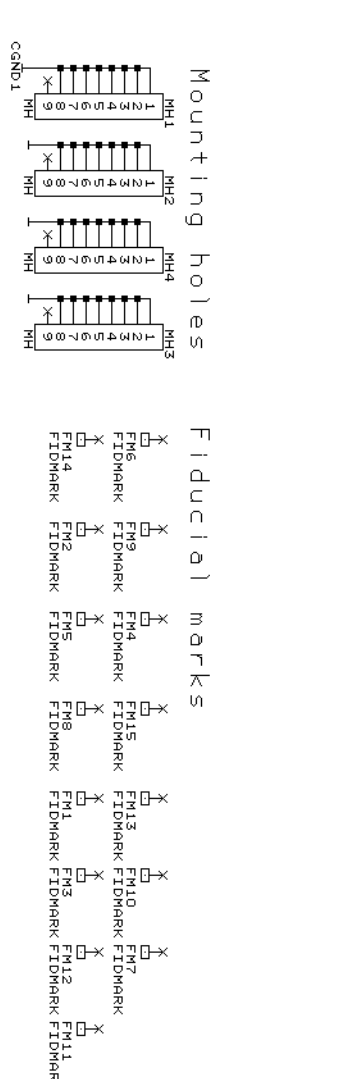
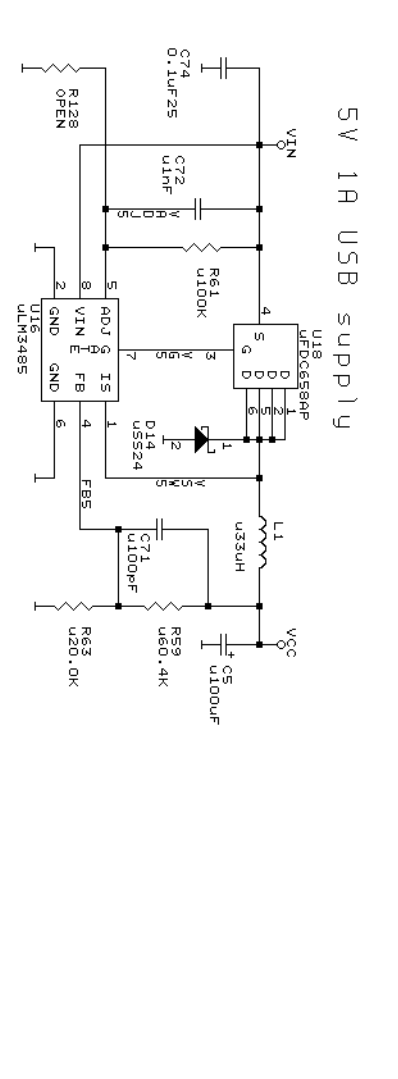
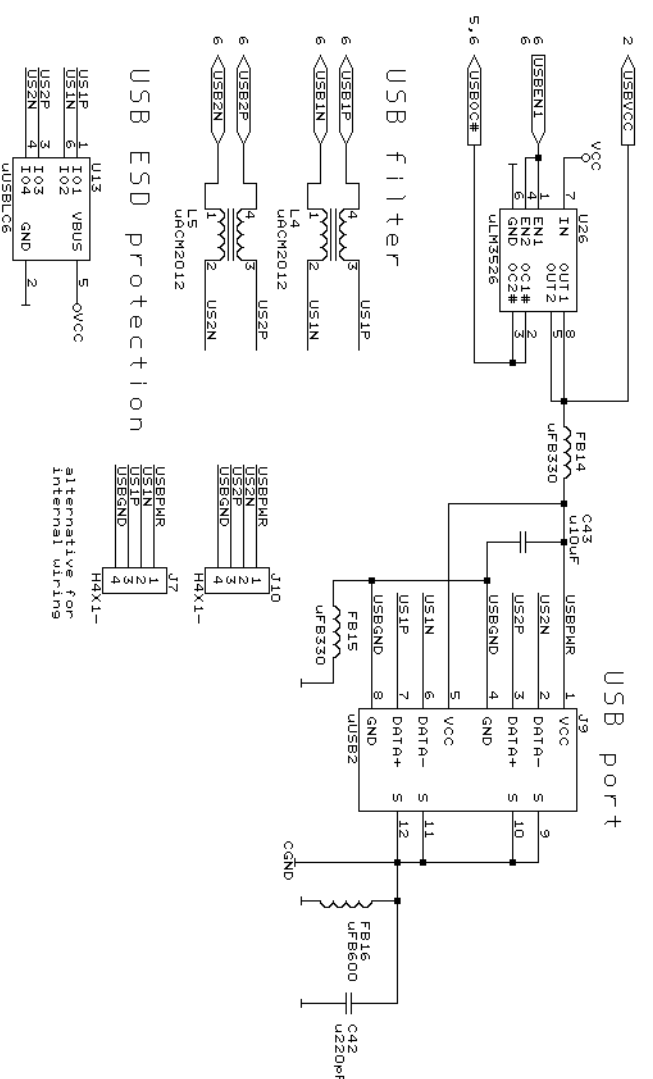
Title	LX + PCI
Size/Document Number	ALIX
Date:	August 12, 2007/Sheet 3 of 10

©2007 PC Engines GmbH  
 CONFIDENTIAL



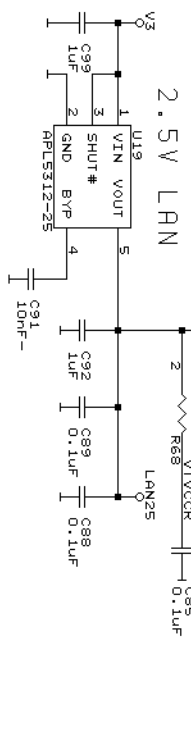
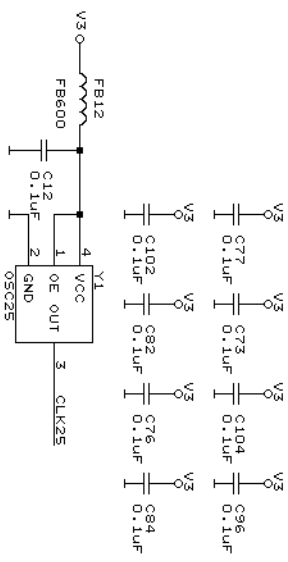
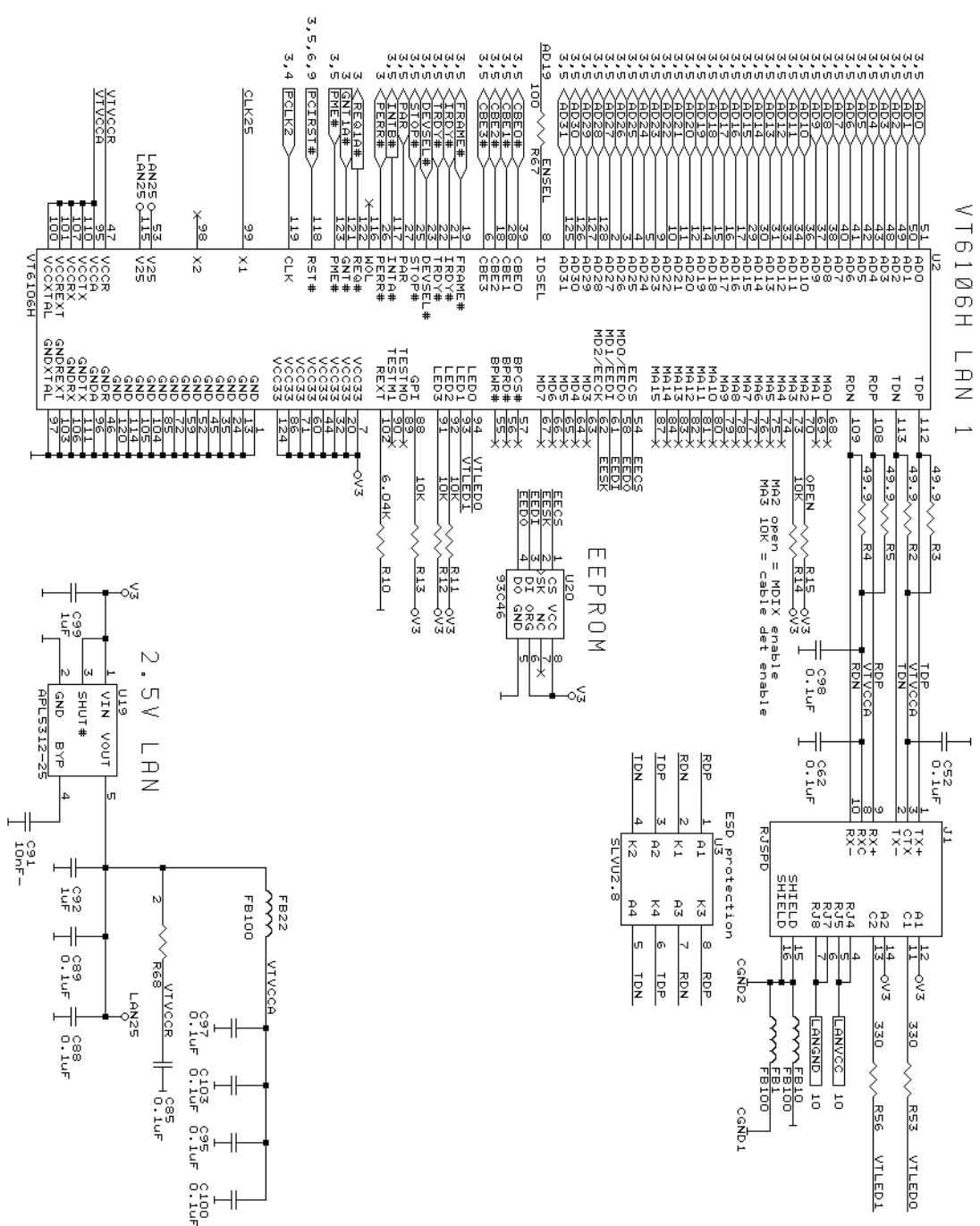






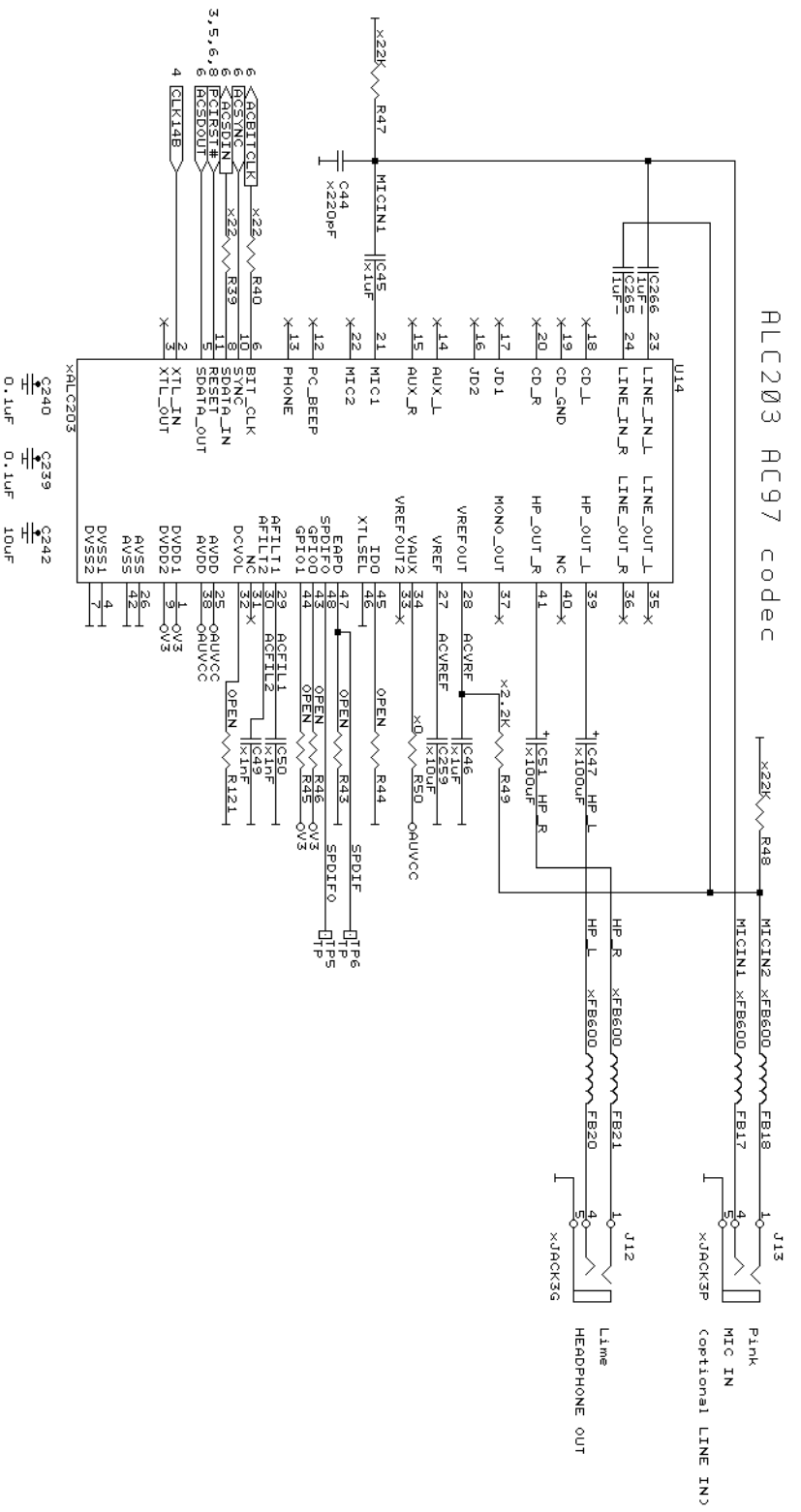
CC)2007 PC Engines GmbH	
CONFIDENTIAL	
Title USB, front panel	
Size Document Number	ALIX
Date:	July 9, 2008 Sheet 7 of 10
REV	30

VT6106H LAN 1

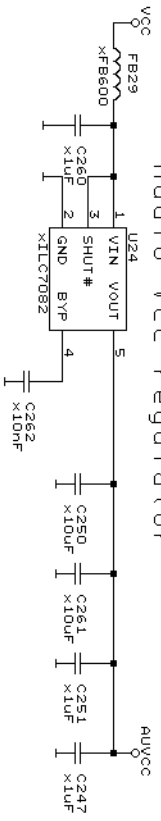




ALC203 AC97 codec



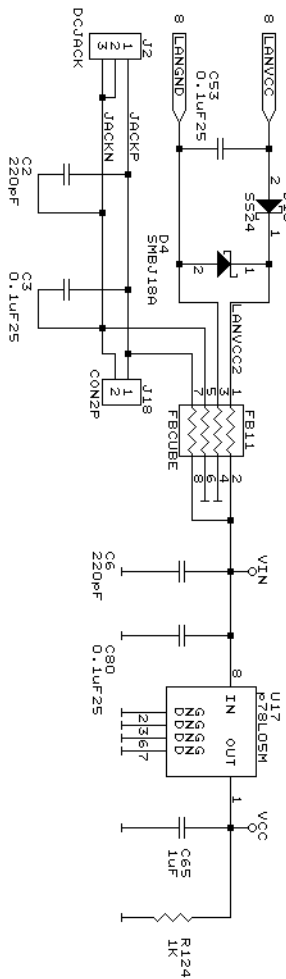
Audio VCC regulator



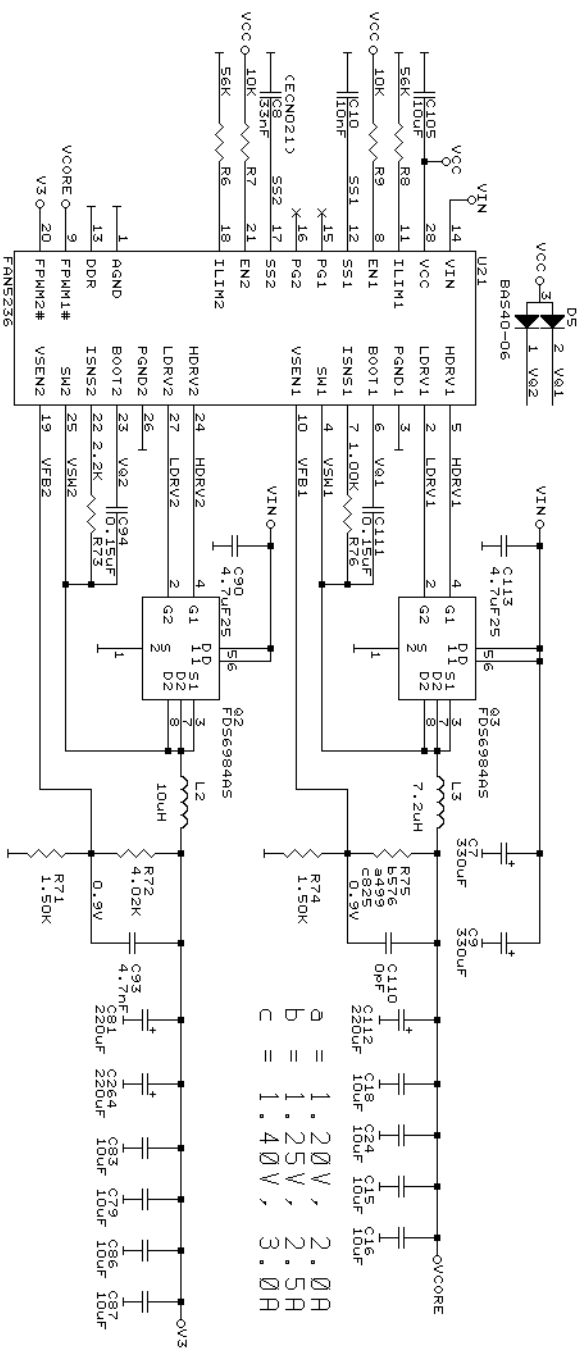
# Power input +7..20V

(also see alternative on sheet 7)

# 5V bias

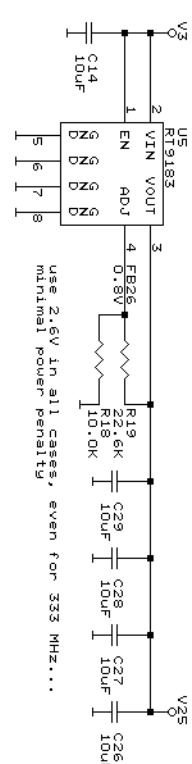


# VCORE, V3 regulator



a = 1.20V, 2.0A  
 b = 1.25V, 2.5A  
 c = 1.40V, 3.0A

# 2.6V DDR supply



use 2.6V in all cases, even for 333 MHz...  
 minimal power penalty